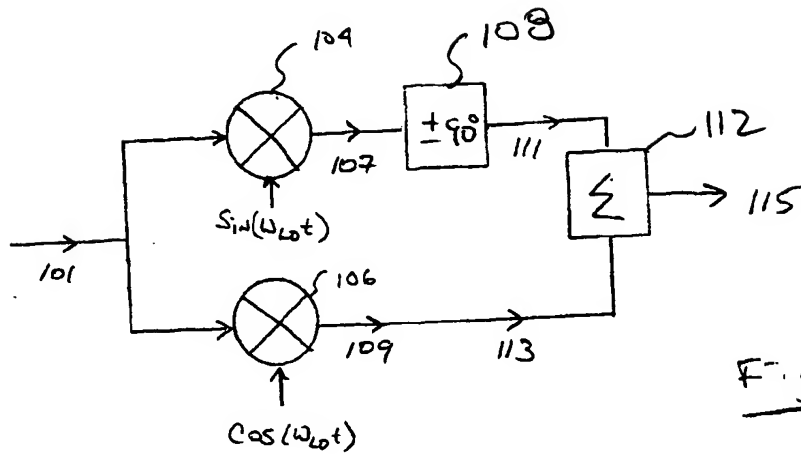


Impedance-Matched IQ Network
for an Image Rejection Circuit

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Fig. 1

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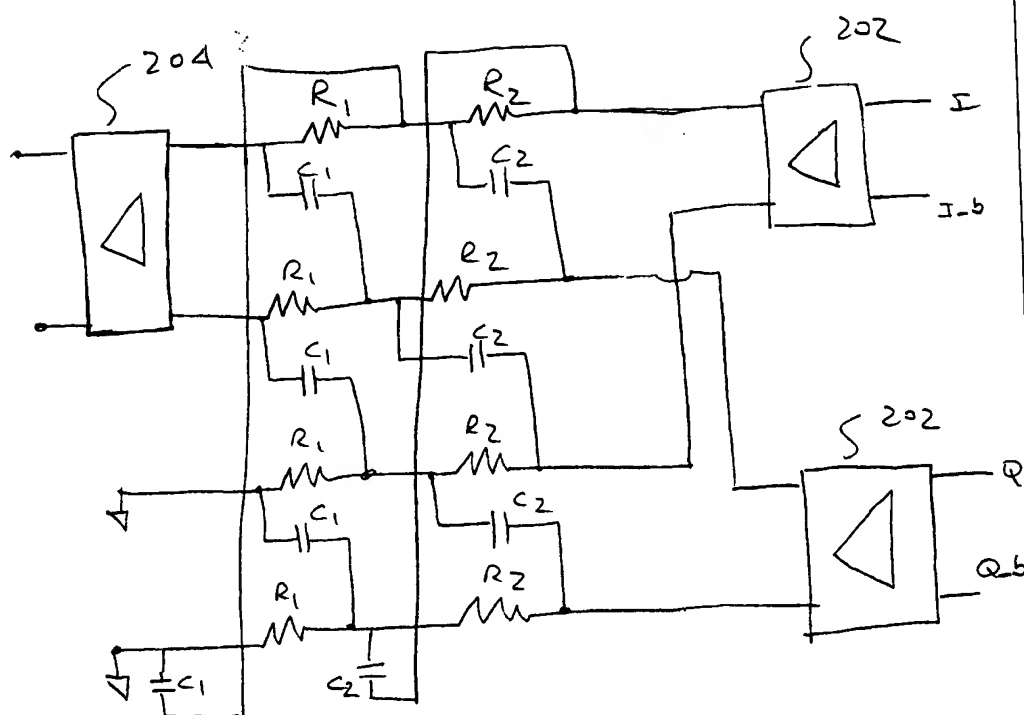


Fig. 2A

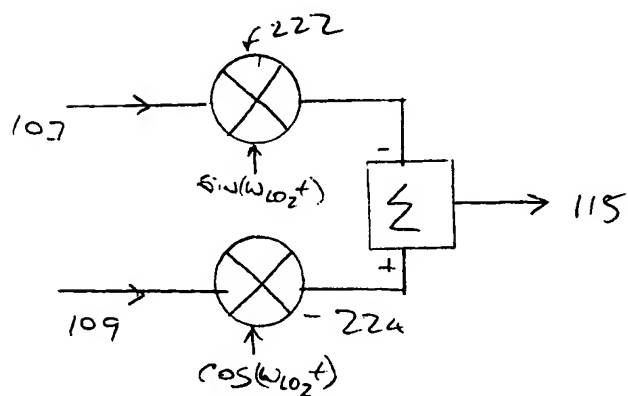
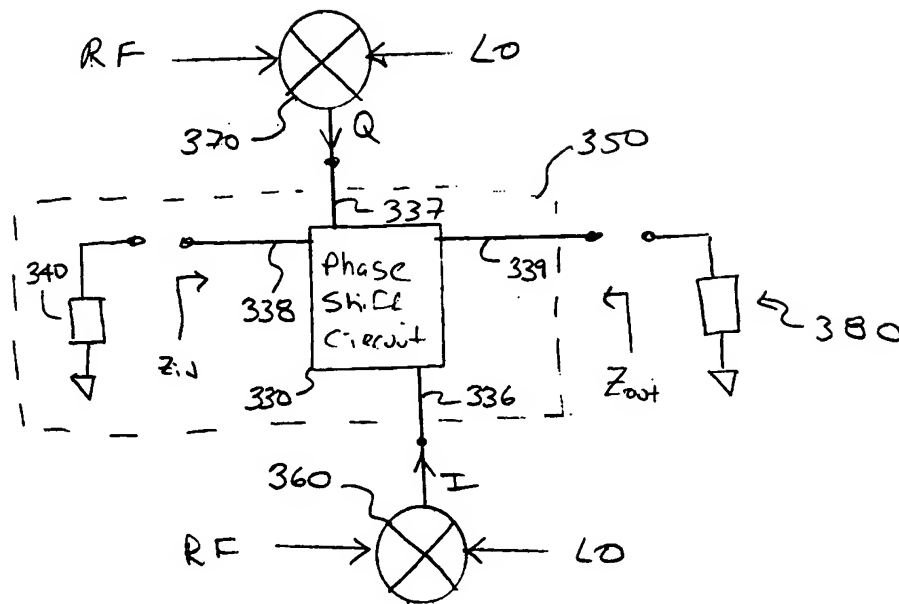
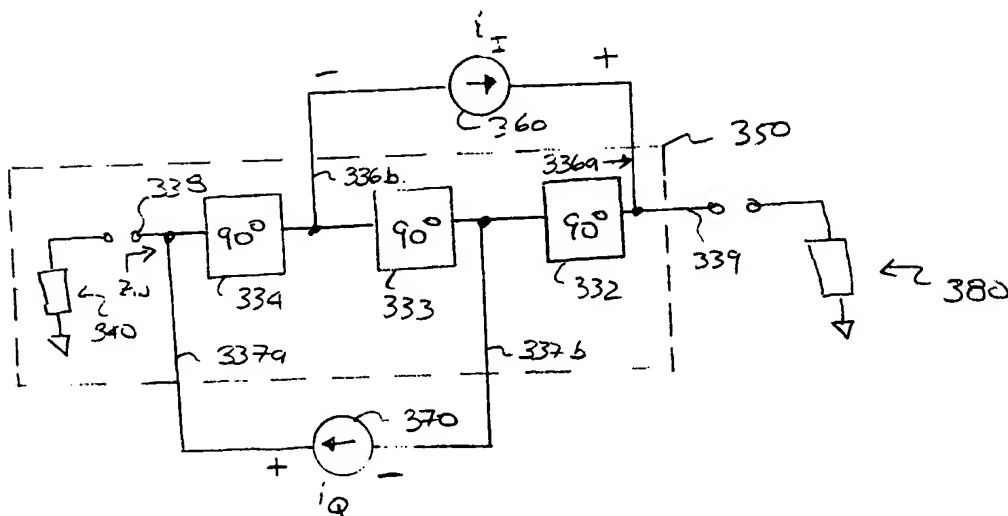


Fig. 2B

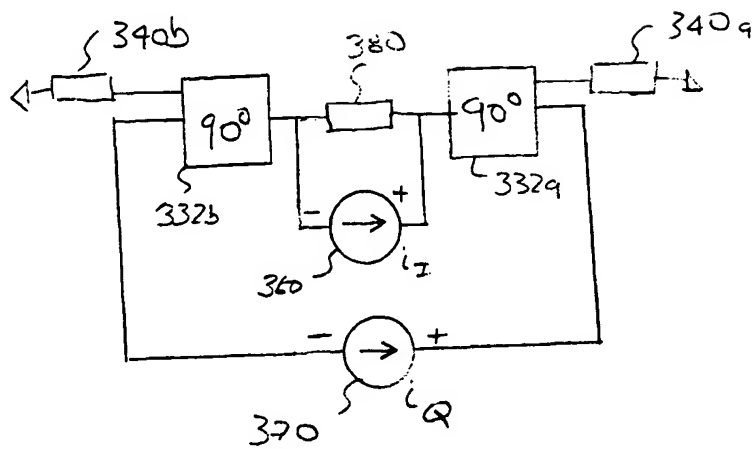
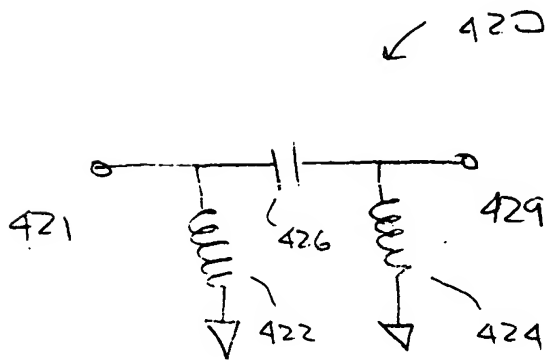
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Fig. 3 AFig. 3 B

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Fig. 3CFig. 4A

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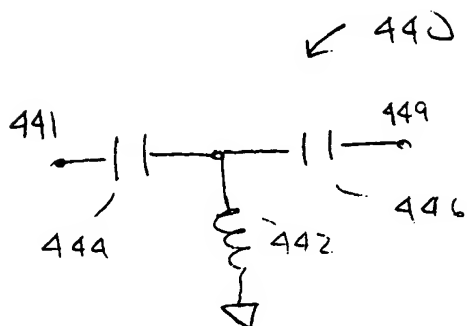


Fig. 4B

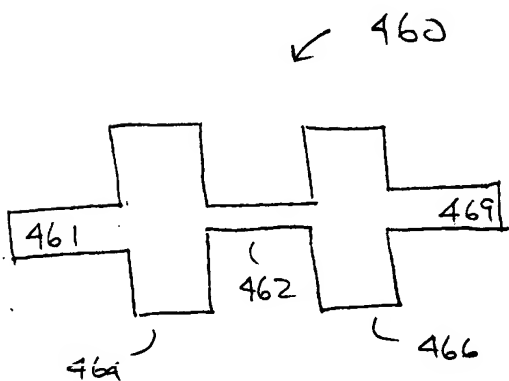


Fig. 4C

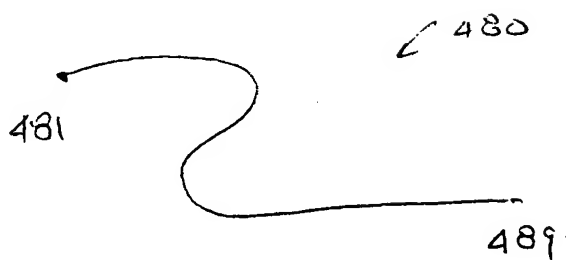


Fig. 4D

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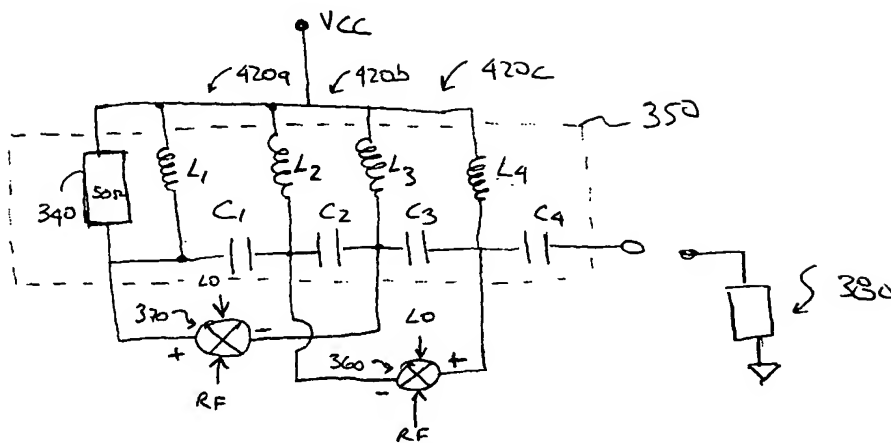


Fig. 5

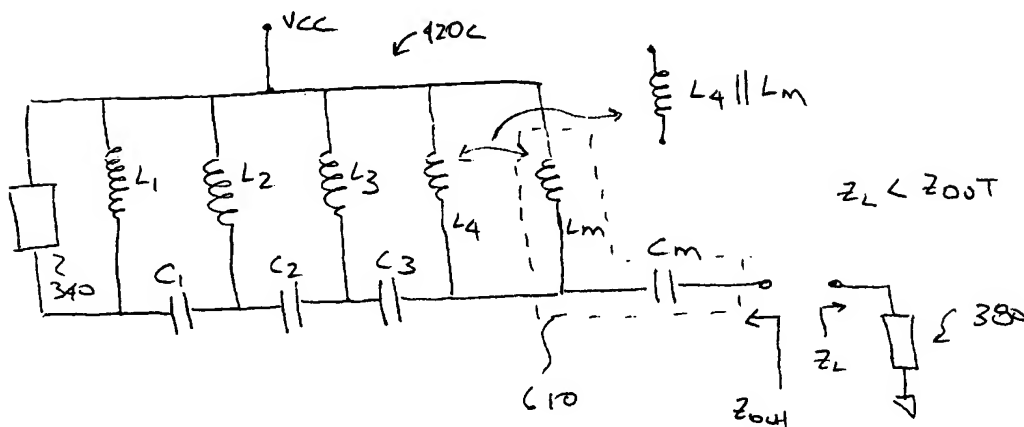


Fig. 6A

Impedance-Matched IQ Network
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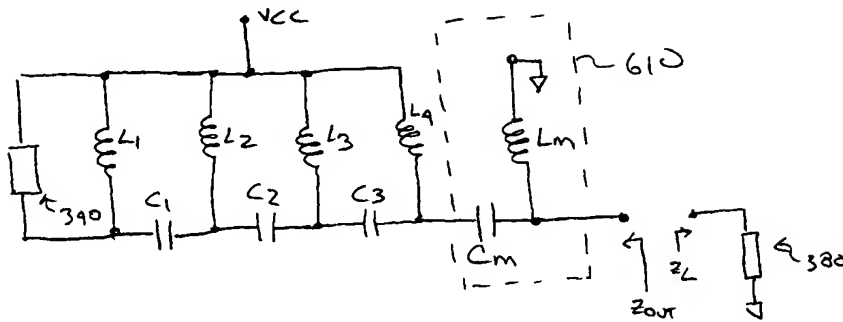


Fig. 6B

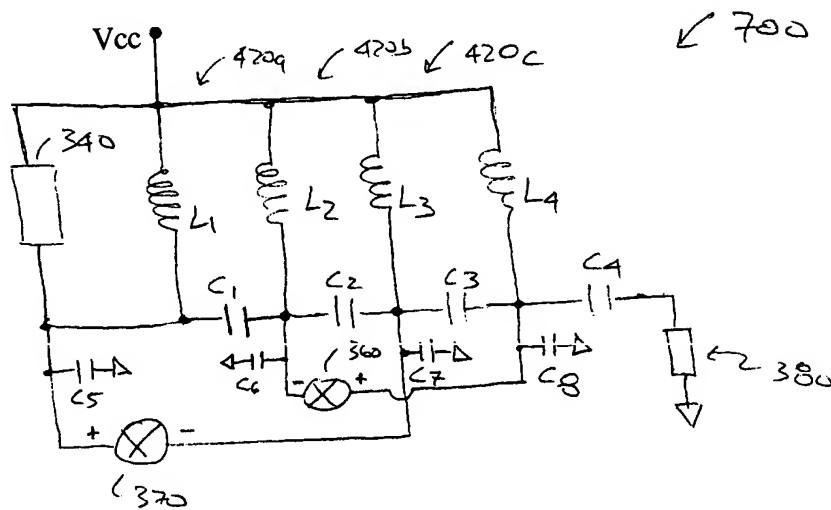


Fig. 7